

What others are saying about this text

I believe that the material is very well presented and constitutes an excellent addition to the literature on CMOS IC layout. When the book becomes available, I would be interested in using it as a supplemental text in a graduate course on VLSI Design Methodology.

—DR. C. ANDRE T. SALAMA, Professor and EE Department Chair
University of Toronto

Bill Bradbury has done a great service to aspiring IC designers by producing this authoritative and engaging text on IC layout. Bill is a veteran designer and a natural teacher. His enthusiasm for his subject shines through in every page. He has decades of experience in IC design, which he has distilled in this up-to-date book. He incorporates the latest in deep-sub-micron CMOS technology, fully illustrated with his own full-color drawings, as well as an incredible number of beautiful and informative photomicrographs of actual IC cross-sections. But this is not a book to savor only for its beauty; it's a book to get down to serious hands-on learning with! Bill has included a CD-ROM containing commercial CAD tools from Mentor Graphics, with serious design capabilities, so that the student can sit down at his or her computer and practice the techniques Bill describes in his book. What a great gift to those of us in the business of teaching and learning IC design. Thanks, Bill!

—DR. WILLIAM AHLGREN, Assistant Professor, Electrical Engineering Department
California Polytechnic State University, San Luis Obispo

One of the signs of an excellent teacher is enthusiasm for the teaching/learning process. Bill Bradbury's enthusiasm shines through in *CMOS IC Layout: A Guide to Topological Chip Design* to the delight of his audience, students, and others interested in CMOS IC layout.

Another sign of teaching excellence is the ability to make the difficult easy to understand. Bill has taken the mystery out of CMOS IC layout and made a complex topic easy to master. There's nothing on the market comparable to this excellent technical text—a true learning text.

CMOS IC Layout: A Guide to Topological Chip Design is a text on how to do it the practical way... the right way. Absolutely, I wholeheartedly recommend it.

—DR. JOHN KARONIKA
University of Houston

CMOS IC Layout: A Guide to Topological Chip Design is a very good text for beginning and experienced layout personnel. It provides many practical insights and guidelines for successful layout strategies. It is a concise and well-organized resource which I highly recommend for anyone seeking a hands-on approach to layout. I wish *CMOS IC Layout: A Guide to Topological Chip Design* had been available for my initial layout projects!

—DR. SAMUEL VILLAREAL, Assistant Professor, EE Department
University of Texas, Dallas